


<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>	Application Number	«ApplicationNo» 0/528254
	Filing Date	«FilingDate»
	First Named Inventor	«First_Inventor»
	Art Unit	«ArtUnit»
	Examiner Name	«Examiner»
	Attorney Docket Number	«DOCKETNO»

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. <sup>1</sup>	Document Number No.-Kind Code <sup>2</sup> (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns Lines, Where Relevant Passages or Relevant Figures Appear
		US-			
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FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. <sup>1</sup>	Document Number (ctry <sup>3</sup> -no.-kind <sup>2</sup> , if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of cited document	Pages, Columns Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>6</sup>

NON-PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in capital letters), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>6</sup>
		HUNG C-M ET AL: "FULLY INTEGRATED 5.35-GHZ CMOS VCOS AND PRESCALERS" IEEE TRANSACTIONS ON MICROWAVE THEORY AND TECHNIQUES, IEEE INC. NEW YORK, US, VOL. 49, NO. 1, 1 JANUARY 2001, PAGES 17-22	

Examiner Signature		Date Considered	3/17/06
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